



Class	Subclass
ISSUE CLASSIFICATION	

PATENT NUMBER

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U.S. UTILITY Patent Application

10 O.I.P.E. PATENT DATE

SCANNED Q.A.

APPLICATION NO. 09/854269	CONT/PRIOR D	CLASS 25 257	SUBCLASS 77-8	ART UNIT 2822	EXAMINER LEWIS
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APPLICANTS

Thomas DiStefano

Microelectronic packages with solder interconnections

FILE **ALBANY**

PTO-2040
12/89

ISSUING CLASSIFICATION

[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS Sheets Drwg. Figs. Drwg. Print Fig.		CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
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	_____ (Primary Examiner) (Date)		Amount Due	Date Paid
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